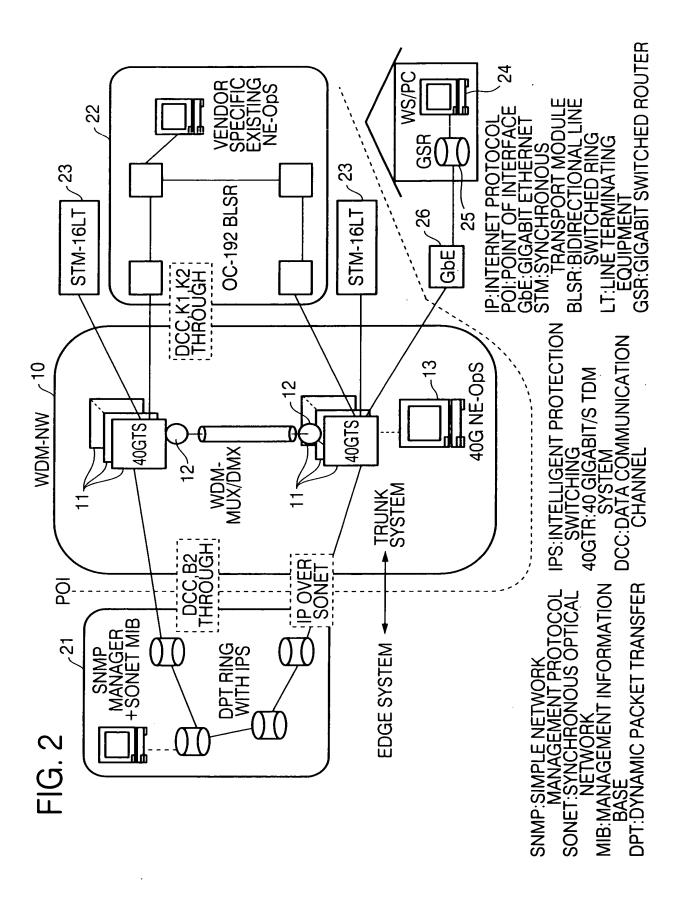
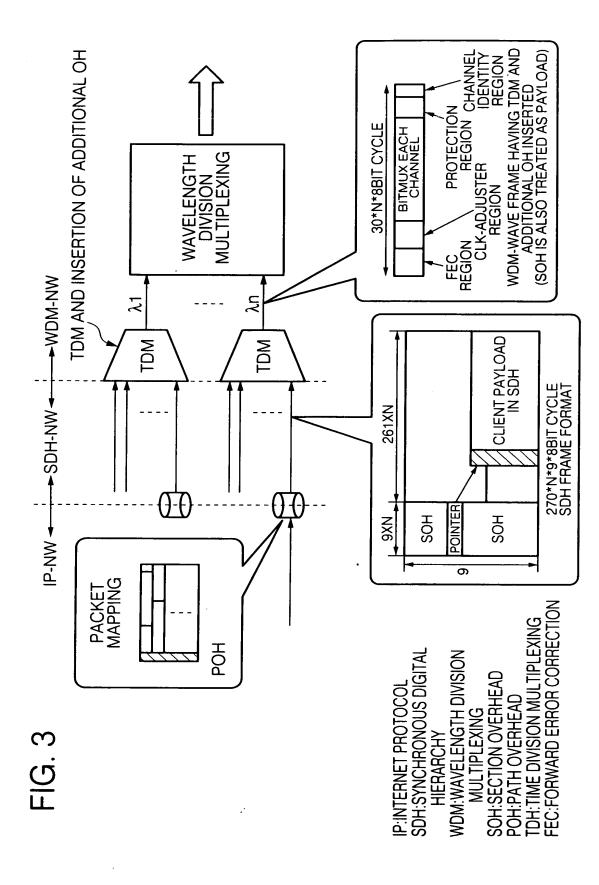
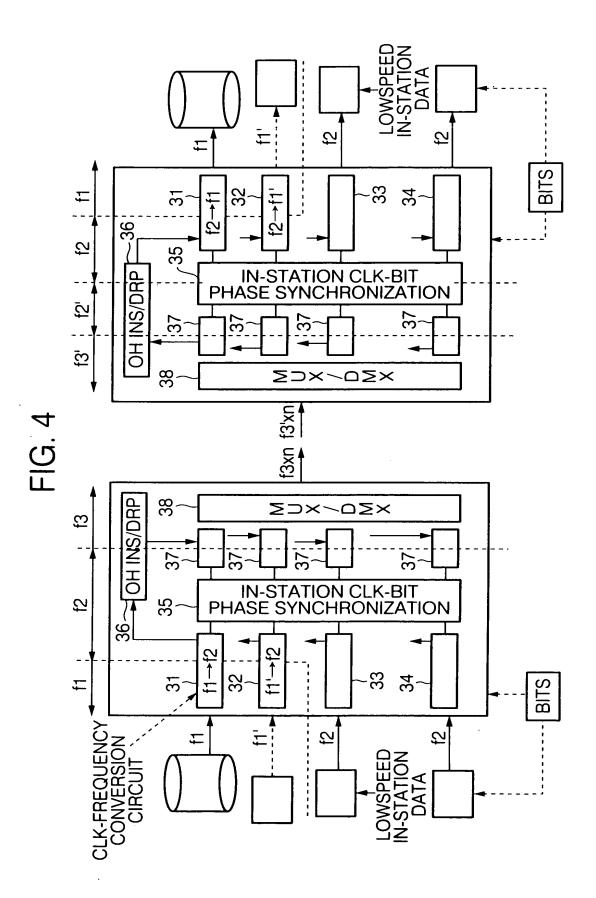
₹ ,

CLIENT PAYLOAD WN-MOW.◆ 270*N*9*8BIT CYCLE SDH FRAME FORMAT 261XN **NN-HQS POINTER** SOH NX6 SOH δ IP-NW IP:INTERNET PROTOCOL SDH:SYNCHRONOUS DIGITAL HIERARCHY WDM:WAVELENGTH DIVISION MULTIPLEXING SOH:SECTION OVERHEAD POH:PATH OVERHEAD PACKET MAPPING POH FIG. .

4







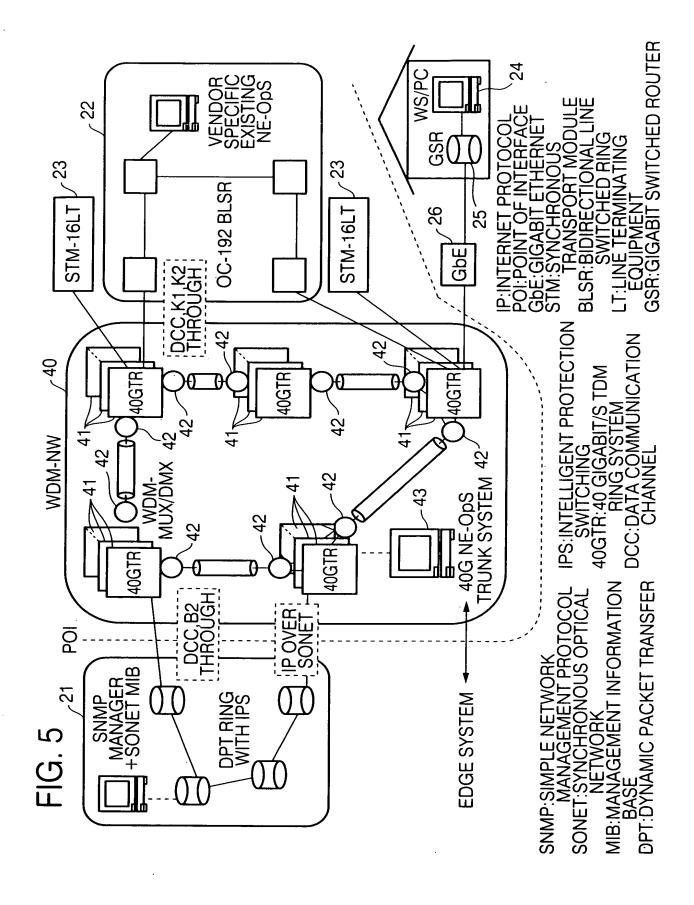
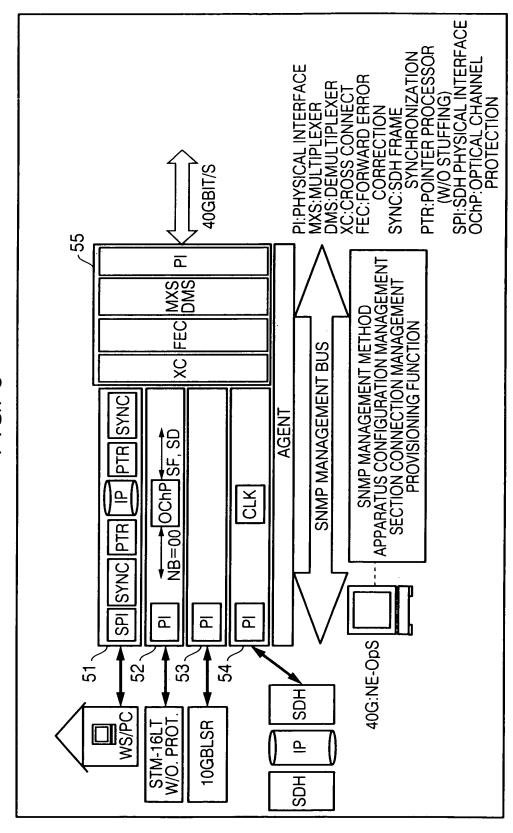


FIG. 6



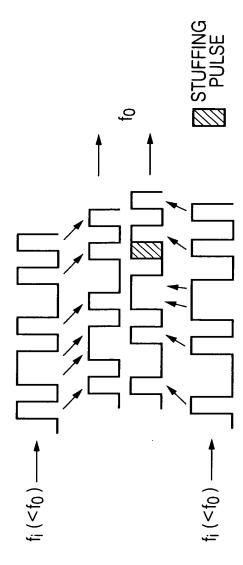
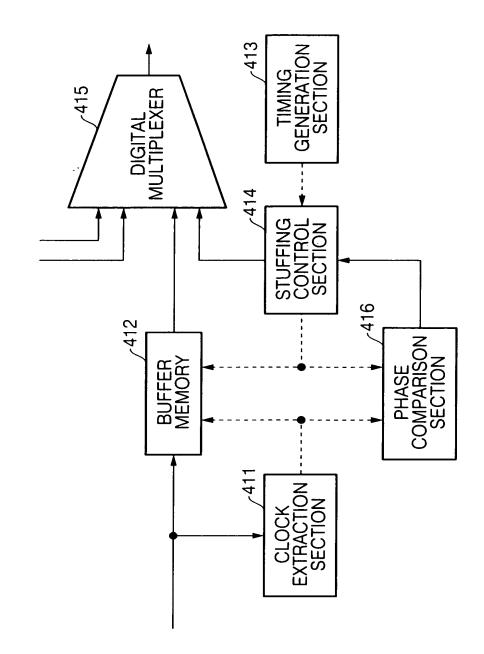


FIG. 8



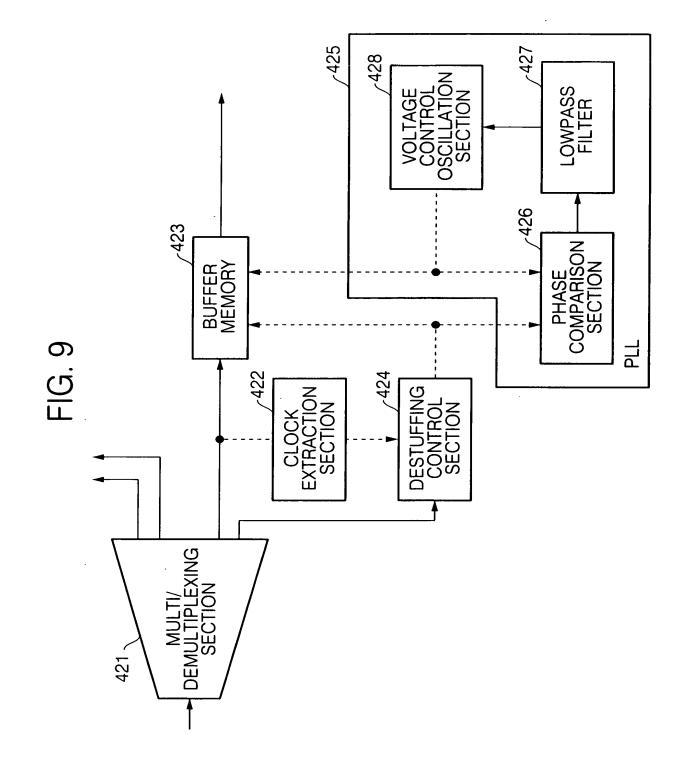


FIG. 10

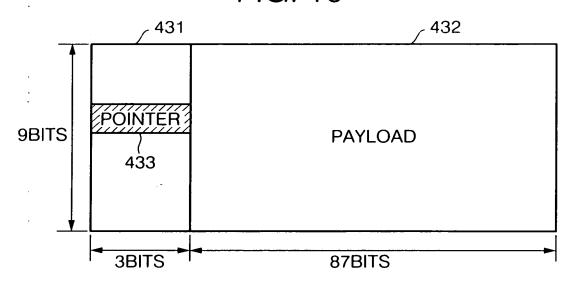
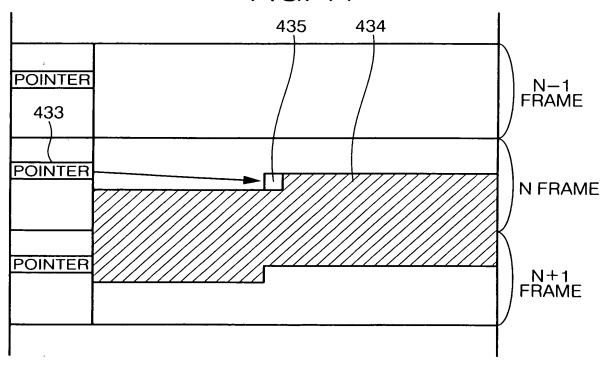
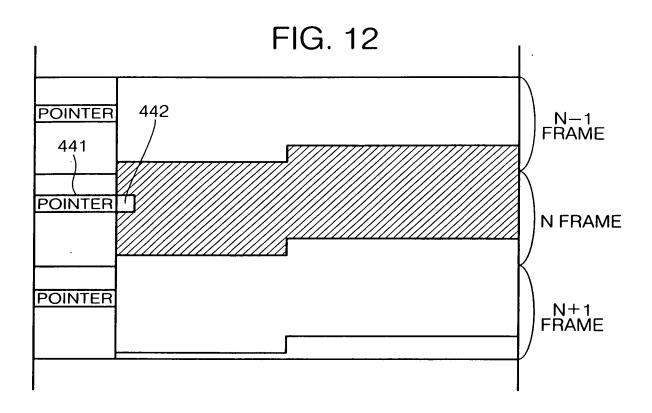
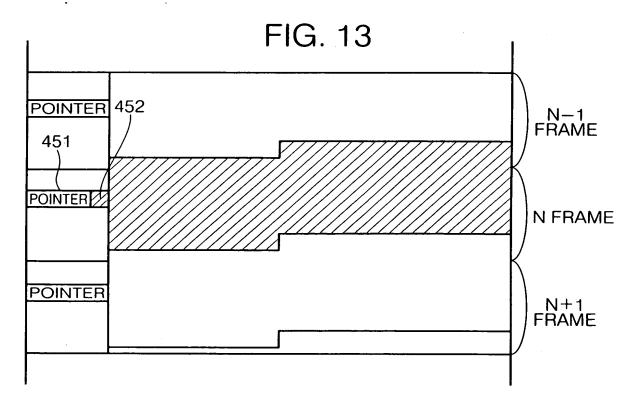
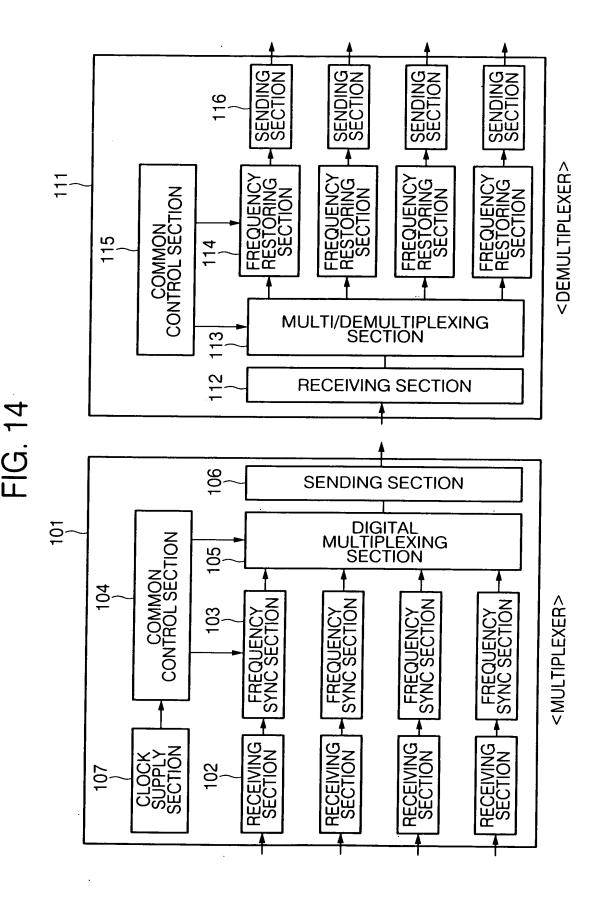


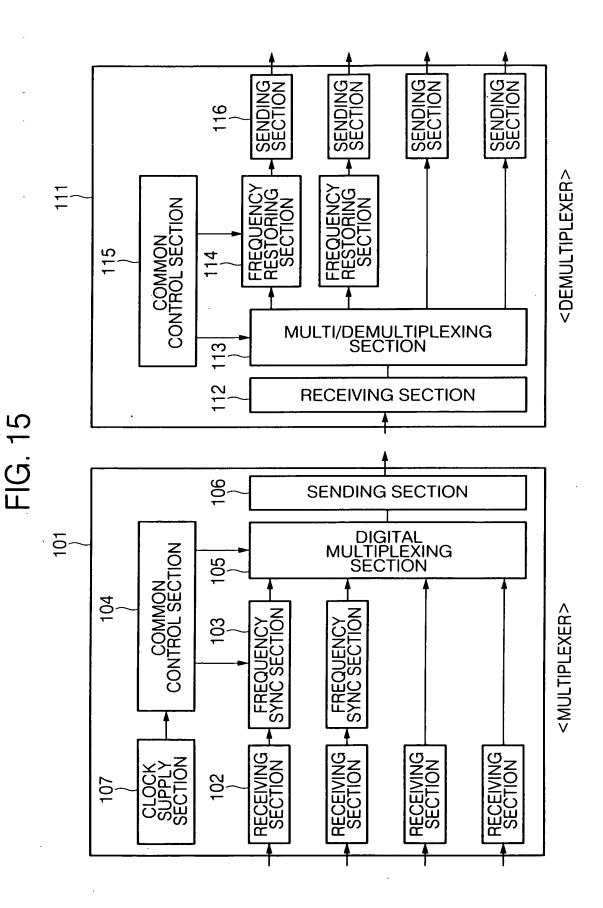
FIG. 11

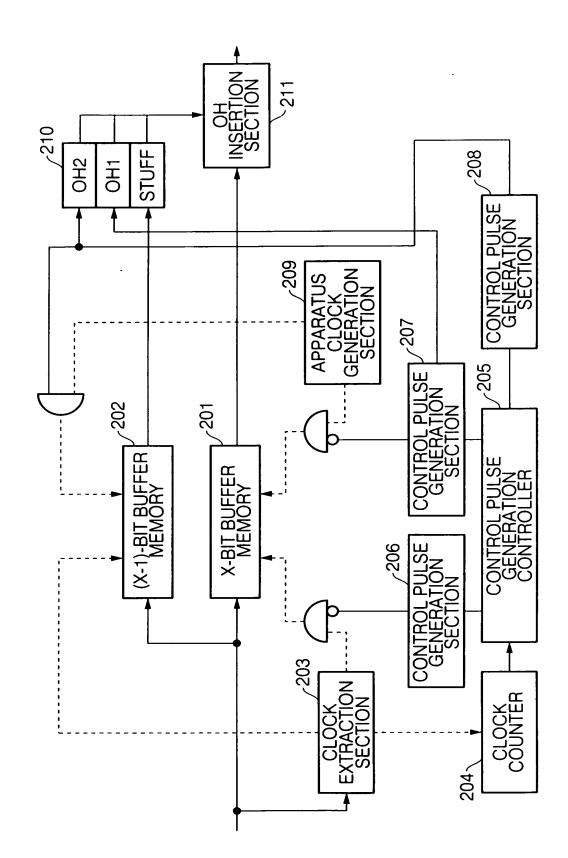












LOWPASS FILTER 309 307 CONTROL | GENERA | SECTION -306 305 310 SELECTOR **TOR** X-BIT BUFFER MEMORY 303 STUFF OH2 문 301 DEMULTIPLEXING SECTION 304

FIG. 17

FIG. 18

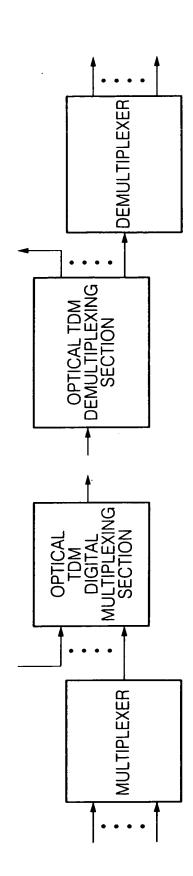


FIG. 19

